

FORM PTO-1449 FIRST SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 0056.10US	APPLICATION NO. 09/223,046
	APPLICANT Hsu et al.	
	FILING DATE December 30, 1998	GROUP 2783 2154

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AK3	5,793,661	08/1998	Dulong et al.	364	750.5	

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL1	WO 97/07450	02/1997	PCT WO	G06F	7700	Yes No
	AM1	WO 97/08608	03/1997	PCT WO	G06F	7700	Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)			
	AQ	2	i860™ Microprocessor Family Programmer's Reference Manual, Intel Corporation, 1992, 79 pages, (Bates Numbers L09361-L09439).
	AR	2	Lee, Ruby, "Accelerating Multimedia with Enhanced Microprocessors," IEEE Micro, IEEE, April, 1995, pp. 22-32, (Bates Numbers L08566-L08576).
	AS	2	DSP56000 24-Bit Digital Signal Processor Family Manual, Motorola, Inc., 1995, 638 pages (Bates Numbers L08722-L09359).




EXAMINER	LARRY D. DONAGHUE PRIMARY EXAMINER	DATE CONSIDERED	03/24/01
EXAMINER: Initial if reference considered, initial if not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.			

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OTHER (Including Author, Title, Date, Pertinent Pages, etc.)





	AP	11	DSP56002/DSP56L002 Motorola Semiconductor Product Information, Motorola, Inc., 1994, 3 pages (Bates Numbers L07913-L07915). ✓
	AQ	11	TMS320C1x/IC2x/IC2xx/IC5x Assembly Language Tools User's Guide, Texas Instruments, March 1995, 483 pages (Bates Numbers L07916-L08398).
	AR	11	TMS320C5x General-Purpose Applications User's Guide, Texas Instruments, July 1997, 167 pages (Bates Numbers L08399-L08565).
	AS		

EXAMINER LARRY D. DONAGHUE PRIMARY EXAMINER	DATE CONSIDERED 02/24/01
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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)			
	AP	10	Peleg, A. and Weiser, U., "MMX Technology Extension To The Intel Architecture," <i>IEEE Micro</i> , IEEE, August, 1996, pp. 42-50, (Bates Numbers L07222-L07230).
	AQ	10	El-Sharkawy, Mohamed, Ph.D., <i>Digital Signal Processing Applications with Motorola's DSP56002 Processor</i> , Prentice Hall PTR, 1996, pp. 43-124 (Bates Numbers L06519-L06601). ✓
	AR	10	DSP56002 24-Bit Digital Signal Processor User's Manual, Motorola, Inc., 1995, 386 pages (Bates Numbers L07277-L07662). ✓
	AS	10	DSP56002 Semiconductor Technical Data, Motorola, Inc., 1996, 110 pages (Bates Numbers L07803-L07912). ✓

EXAMINER LARRY D. DONAGHUE PRIMARY EXAMINER	DATE CONSIDERED 05/24/01
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<p>FORM PTO-1449</p> <p><u>INFORMATION DISCLOSURE STATEMENT</u></p>	<p>ATTY. DOCKET NO.</p> <p>0056.10US</p>	<p>APPLICATION NO.</p> <p>09/223,046</p>
	<p>APPLICANTS</p> <p>Hsu <i>et al.</i></p>	
	<p>FILING DATE</p> <p>December 30, 1998</p>	<p>GROUP</p> <p>2703 2154</p>

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AP		
	AQ		
	AR		
4	AS	11	<p>Diefendorff, K., "The 88110: A Superscalar Microprocessor with Graphics Support" (Motorola) Microprocessor Forum, November, 1991, 20 pages, (Bates Numbers L00622-00641).</p> <p>LARRY D. DONALDSON</p>

EXAMINER

DATE CONSIDERED


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U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA2						
	AB2						
	AC2						
	AD2						
	AE2						
	AF2						
	AG2						
	AH2						
	AI2						
	AJ2						
	AK2						

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL2						Yes No
	AM2						Yes No
	AN2						Yes No
	AO2						Yes No
	AP2						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AP	12	Diefendorff, K. and Allen, M., "Organization of the Motorola 88110: A Superscalar RISC Microprocessor." Motorola Inc., date unknown, 12 pages, (Bates Numbers L00642-00653).
	AQ		
	AR		
	AS		

EXAMINER 	DATE CONSIDERED 03/04/01
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